



ISATA-SLIM(T)-xxxG

Disk on Module

(TLC type)

Version 1.0

Features:

- SATA III
- Kioxia 112 layers 3D TLC NAND
- Standard & Wide-temperature
- Dynamic Thermal Management
- Hybrid Write

Reliability:**Performance:**

- Sequential Read up to 550 MB/s
- Sequential Write up to 510 MB/s

Power Requirements:

Input Voltage:	5V±5%
Max Operating Wattage:	2.7W
Idle Wattage:	1.0W

Capacity	TBW	DWPD
128GB	150	1.09
256GB	300	1.09
512GB	600	1.09
1TB	1200	1.09
2TB	2400	1.09

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REVISION HISTORY

Revision	Description	Date
Preliminary	First Released	Dec., 2023

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1. Product Overview

1.1 Introduction of ISATA-SLIM(T)

ICOP ISATA-SLIM(T)-xxxG products provide high capacity flash memory Solid State Drive (SSD) that electrically complies with Serial ATA (SATA) standard. It supports SATA III standard (6.0GHz) with high performance, achieves excellent performance up to 4CH standard by cost effective controller with 4CH.

With L³ FW architecture, combining our signature 4K mapping algorithm L² FW architecture with powerful LDPC technology, ISATA-SLIM(T) series has outstanding high IOPS, better data integrity and extended lifespan through reducing the bad block number happening.

For real industrial application, ISATA-SLIM(T) series is built in thermal sensor to monitor the environment temperature.

CAUTION *TRIM must be enabled.*

TRIM enables SSD's controller to skip invalid data instead of moving. It can free up significant amount of resources, extends the lifespan of SSD by reducing erase, and write cycles on the SSD. ICOP's handling of garbage collection along with TRIM command improves write performance on SSDs.

1.2 Product View and Models

ISATA-SLIM(T)-xxxG is available in follow capacities within TLC flash ICs.

ISATA-SLIM(T)-128G 128GB ISATA-SLIM(T)-256G 256GB ISATA-SLIM(T)-512G 512GB
ISATA-SLIM(T)-1T 1TB ISATA-SLIM(T)-2T 2TB



Figure 1: ISATA-SLIM(T)-xxxG

1.3 SATA Interface

ISATA-SLIM(T)-xxxG supports SATA III(6.0Gb/s) interface, and compliant with SATA I (1.5Gb/s) and SATA II(3.0Gb/s).

1.4 MO-297 Form Factor

ISATA-SLIM(T)-xxxG has a compact design 54.0mm (W) x 39.0mm (L) x 4.0mm (H) without metal material case, and is easy for installation.

2. Product Specifications

2.1 Capacity and Device Parameters

ISATA-SLIM(T)-xxxG device parameters are shown in Table 1.

Table 1: Device parameters

Capacity	Cylinders	Heads	Sectors	LBA	User Capacity(MB)
128GB	16383	16	63	234441648	114473
256GB	16383	16	63	468862128	228936
512GB	16383	16	63	937703088	457862
1TB	16383	16	63	1875385008	915715
2TB	16383	16	63	3750748848	1831420

2.2 Performance

Burst Transfer Rate: 6.0Gbps

Table 2: Performance

Capacity	Unit	128GB	256GB	512GB	1TB	2TB
Sequential ² Read (Q32T1)	MB/s	440	550	550	550	550
Sequential ² Write (Q32T1)		290	480	490	510	470
Sustained ³ Sequential Read (Avg.)		350	420	420	420	410
Sustained ³ Sequential Write (Avg.)		85	160	310	320	280
4KB Random ² Read (Q32T1)	IOPS	43,000	80,000	83,000	83,000	83,000
4KB Random ² Write (Q32T1)		29,000	42,000	75,000	72,000	76,000

Note:

- Performance may vary based on various firmware version or test platform. In addition, Kioxia 3D TLC series adopt hybrid mode which enables SLC Cache up to 3% of total user.
- Performance results are based on CrystalDiskMark 6.0.2 with file size 1000MB of Queue Depth32.
- Performance results are based on AIDA 64 v5.98 with block size 1MB of Linear Read & Write Test Item

2.3 Electrical Specifications

2.3.1 Power Requirement

Table 3: ICOP ISATA-SLIM(T)-xxxG Power Requirement

Item	Symbol	Rating	Unit
Input voltage	V _{IN}	+5 DC +- 5%	V

2.3.2 Power Consumption

Table 4: Typical Power Consumption

Mode	Power consumption (W)
Read	1.8
Write	1.9
Idle	1.0
Boot-Up	5.3

Note: Current results may vary depending on system components and power circuit design.

2.4 Environmental Specifications

2.4.1 Temperature Ranges

Table 5: Temperature range for ISATA-SLIM(T)-xxxG

Temperature	Range
Operating	Standard Grade: 0°C to +70°C
	Industrial Grade: -40°C to +85°C
Storage	-40°C to +85°C

2.4.2 Humidity

Relative Humidity: 10-95%, non-condensing

2.4.3 Shock and Vibration

Table 6: Shock/Vibration Testing for ISATA-SLIM(T)-xxxG

Reliability	Test Conditions	Reference Standards
Vibration	7 Hz to 2K Hz, 20G, 3 axes	IEC 60068-2-6
Mechanical Shock	Duration: 0.5ms, 1500 G, 3 axes	IEC 60068-2-27

2.4.4 Mean Time between Failures (MTBF)

The following table summarizes the MTBF prediction results for various ISATA-SLIM(T)-xxxG

configurations. The analysis was performed using a RAM Commander™ failure rate prediction.

- **Failure Rate:** The total number of failures within an item population, divided by the total number of life units expended by that population, during a particular measurement interval under stated condition.
- **Mean Time between Failures (MTBF):** A basic measure of reliability for repairable items: The mean number of life units during which all parts of the item perform within their specified limits, during a particular measurement interval under stated conditions.

Table 7: ISATA-SLIM(T)-xxxG MTBF

Product	Condition	MTBF (Hours)
ISATA-SLIM(T)-xxxG	Telcordia SR-332 GB, 25°C	>3,000,000

2.5 CE and FCC Compatibility

ISATA-SLIM(T)-xxxG conforms to CE and FCC requirements.

2.6 RoHS Compliance

ISATA-SLIM(T)-xxxG is fully compliant with RoHS directive.

2.7 Reliability

Parameter		Value
Flash endurance		3,000 P/E cycles
Error Correct Code		Support
Data Retention		Under 40 C: 10 Years at Initial NAND Status; 1 Year at NAND Life End
TBW* (Total Bytes Written) Units: TB		
Capacity	Sequential workload	Client workload
128GB	341	150
256GB	682	300
512GB	1364	600
1TB	2727	1200
2TB	5455	2400
<p>* Note:</p> <ol style="list-style-type: none"> 1. Sequential: Mainly sequential write, tested by Vdbench. 2. Client: Follow JESD218 Test method and JESD219A Workload, tested by ULINK. 3. Based on out-of-box performance. 		

2.8 Transfer Mode

ISATA-SLIM(T)-xxxG support following transfer mode:

Serial ATA III 6.0Gbps

Serial ATA II 3.0Gbps

Serial ATA I 1.5Gbps

2.9 Pin Assignment

ISATA-SLIM(T)-xxxG uses a standard SATA pin-out.

See following table for ISATA-SLIM(T)-xxxG pin assignment.

Table 8: ISATA-SLIM(T)-xxxG Pin Assignment

Name	Type	Description
S1	GND	NA
S2	A+	Differential Signal Pair A
S3	A-	
S4	GND	NA
S5	B-	Differential Signal Pair B
S6	B+	
S7	GND	NA
Key and Spacing separate signal and power segments		
P1	NC	NA
P2	NC	NA
P3	NC	NA
P4	GND	NA
P5	GND	NA
P6	GND	NA
P7	V5	5V Power, Pre-Charge
P8	V5	5V Power
P9	V5	5V Power
P10	GND	NA
P11	DAS/DSS	Device Activity Signal / Disable Staggered
P12	GND	NA
P13	NC	NA
P14	NC	NA
P15	NC	NA

2.10 Mechanical Dimensions

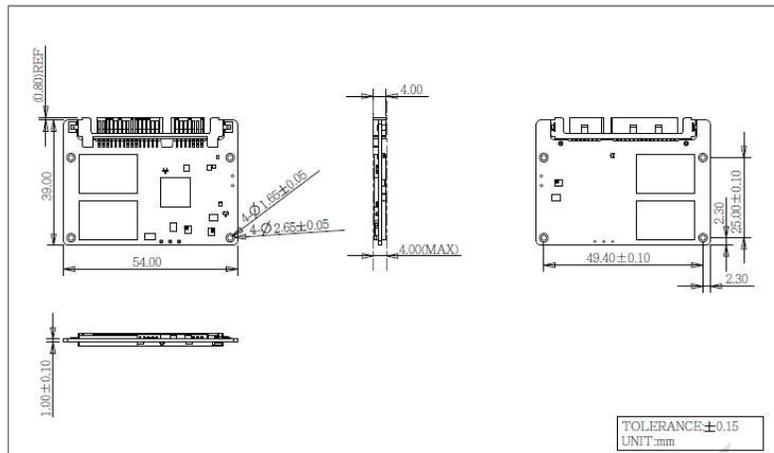


Figure 2 : ISATA-SLIM(T)- xxxG PCBA diagram

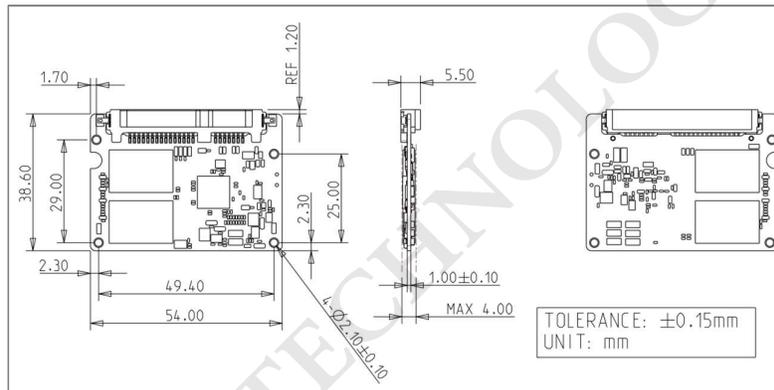


Figure 3 : ISATA-SLIM(T)- xxxG (Inside) PCBA diagram

2.11 Assembly Weight

An ISATA-SLIM(T)-xxxG within flash ICs, 2TB's weight is 13 grams approximately.

2.12 Seek Time

ISATA-SLIM(T)-xxxG is not a magnetic rotating design. There is no seek or rotational latency required.

2.13 NAND Flash Memory

ISATA-SLIM(T)-xxxG uses 3D TLC NAND flash memory, with 3,000 program & erase cycles, which is non-volatility, high reliability and high speed memory storage.

3. Theory of Operation

3.1 Overview

Figure 4 shows the operation of ISATA-SLIM(T)-xxxG from the system level, including the major hardware blocks.

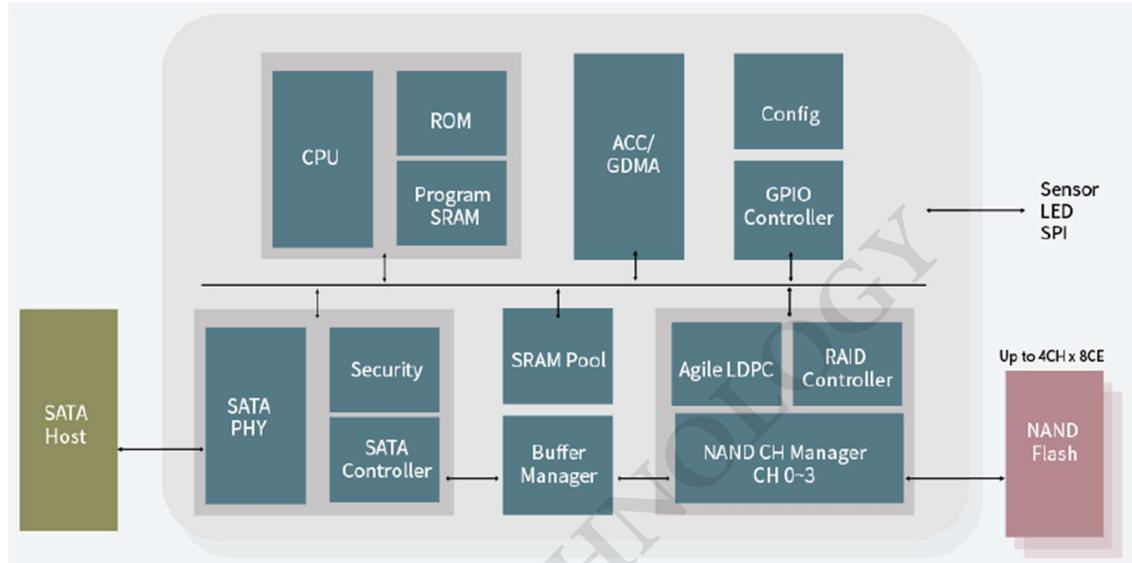


Figure 4: ISATA-SLIM(T)-xxxG Block Diagram

ISATA-SLIM(T)-xxxG integrates a SATA III controller and NAND flash memories. Communication with the host occurs through the host interface, using the standard ATA protocol. Communication with the flash device(s) occurs through the flash interface.

3.2 SATA III Controller

ISATA-SLIM(T)-xxxG is designed with a SATA III 6.0Gbps (Gen. 3) controller. The Serial ATA physical, link and transport layers are compliant with Serial ATA Gen 1, Gen 2 and Gen 3 specification (Gen 3 supports 1.5Gbps/3.0Gbps/6.0Gbps data rate). The controller has 4 channels for flash interface.

3.3 Error Detection and Correction

ISATA-SLIM(T)-xxxG is designed with hardware LDPC ECC engine with hard-decision and Soft-decision decoding. Low-density parity-check (LDPC) codes have excellent error correcting Performance close to the Shannon limit when decoded with the belief-propagation (BP) algorithm using soft-decision information.

3.4 Wear-Leveling

Flash memory can be erased within a limited number of times. This number is called the **erase cycle limit** or **write endurance limit** and is defined by the flash array vendor. The erase cycle limit applies to each individual erase block in the flash device.

ISATA-SLIM(T)-xxxG uses a static wear-leveling algorithm to ensure that consecutive writes of a specific sector are not written physically to the same page/block in the flash. This spreads flash media usage evenly across all pages, thereby extending flash lifetime.

3.5 Bad Blocks Management

Bad Blocks are blocks that contain one or more invalid bits whose reliability are not guaranteed. The Bad Blocks may be presented while the SSD is shipped, or may develop during the life time of the SSD. When the Bad Blocks is detected, it will be flagged, and not be used anymore. The SSD implement Bad Blocks management, Bad Blocks replacement, Error Correct Code to avoid data error occurred. The functions will be enabled automatically to transfer data from Bad Blocks to spare blocks, and correct error bit.

3.6 Garbage Collection

Garbage collection is used to maintain data consistency and perform continual data cleansing on SSDs. It runs as a background process, freeing up valuable controller resources while sorting good data into available blocks, and deleting bad blocks. It also significantly reduces write operations to the drive, thereby increasing the SSD's speed and lifespan.

3.7 Trim

The Trim command is designed to enable the operating system to notify the SSD which pages no longer contain valid data due to erases either by the user or operating system itself. During a delete operation, the OS will mark the sectors as free for new data and send a Trim command to the SSD to mark them as not containing valid data. After that the SSD knows not to preserve the contents of the block when writing a page, resulting in less write amplification with fewer writes to the flash, higher write speed, and increased drive life.

3.8 Die RAID

Die RAID is a controller function which leveraged user capacity to back up the data in NAND flash. Die RAID supported can ensure the user data in the NAND Flash more consistent in certain scenario. ISATA-SLIM(T)-xxxG series is default enable the Die RAID function for the industrial application.

3.9 Quick Erase Command

- Protocol: No Data

- Inputs**Table 9: Execute Quick Erase command for inputs information**

Register	7	6	5	4	3	2	1	0
Features	21h							
Sector Count	41h							
LBA Low	Na							
LBA Mid	Na							
LBA High	Na							
Device	1	1	1	0	Na			
Command	82h							

-Normal Outputs**Table 10: Quick Erase command for normal output information**

Register	7	6	5	4	3	2	1	0
Error	Na							
Sector Count	Na							
LBA Low	Na							
LBA Mid	Na							
LBA High	Na							
Device	obs	Na	obs	DEV	Na	Na	Na	Na
Status	BSY	DRDY	DF	Na	DRQ	Na	Na	ERR

Device register-

DEV shall specify the selected device.

Status register

BSY will be cleared to zero indicating command completion

DRDY will be set to one.

DF (Device Fault) will be cleared to zero.

DRQ will be cleared to zero

ERR will be cleared to zero.

3.10 SLC cache

Table 11: ISATA-SLIM(T)-xxxG SLC cache

Capacity	128GB	256GB	512GB	1TB	2TB
SLC cache (GB)	3	5	9	18	36
SLC cache (%)	2.3	1.9	1.7	1.7	1.7

ISATA-SLIM(T) series adopt hybrid mode which enables SLC Cache up to 3% of total user capacity by TLC direct write to strike balance between burst performance and steady overall stability.

3.11 Thermal Throttling

Thermal throttling is a protective mechanism designed to safeguard components from potential damage caused by excessive temperatures. When an SSD approaches a critical temperature threshold, firmware activates the thermal throttling mechanism to regulate the SSD's temperature. Thermal throttling is crucial for SSDs since it prevents drive damage, which could otherwise result in data loss. However, it's worth noting that when thermal throttling is activated, read and write tasks may experience a reduction in speed.

4. Installation Requirements

4.1 ISATA-SLIM(T)-xxxG Pin Directions

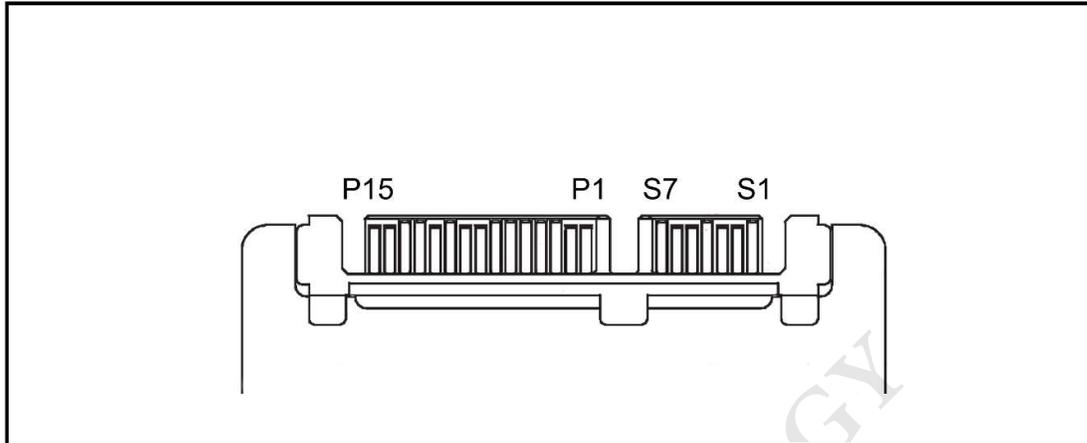


Figure 5: Signal Segment and Power Segment

4.2 Electrical Connections for ISATA-SLIM(T)-xxxG

A Serial ATA device may be either directly connected to a host or connected to a host through an adaptor card. The SATA interface has a separate connector for the power supply. Please refer to the pin description for further details.

4.3 Device Drive

No additional device drives are required. The ICOP ISATA-SLIM(T)-xxxG can be configured as a boot device.

5. SMART Feature Set

ISATA-SLIM(T)-xxxG series support the SMART command set and defines some vendor-specific data to report SMART attributes of SSD.

Table 12: SMART command

Value	Command	Value	Command
D0h	Read Data	D5h	Read Log
D1h	Read Attribute Threshold	D6h	Return Status
D2h	Enable/Disable Auto save	D8h	Enable SMART Operations
D3h	Save Attribute Values	D9h	Disable SMART Operations
D4h	Execute OFF-LINE Immediate	DAh	Return Status

5.1 SMART Attributes

ISATA-SLIM(T)-xxxG series SMART data attributes are listed in following table.

Table 13: SMART attribute

Attribute ID (hex)	Value	Raw Attribute Value						Rsv	Attribute Name
01	X								Read Error Rate
05	X	LSB	MSB	00	00	00	00	00	Later Bad
09	LSB	LSB	MSB	00	00	00	00	00	Power-On hours Count
0C	LSB	LSB	MSB	00	00	00	00	00	Drive Power Cycle Count
A3	X	LSB			MSB	00	00	00	Total Bad Block Count
A5	LSB	LSB			MSB	00	00	00	Max Erase count
A7	LSB	LSB			MSB	00	00	00	Avg Erase count
A9	LSB	LSB	00	00	00	00	00	00	Device Life
AA	X	LSB	MSB	00	00	00	00	00	Spare Block Count
AB	LSB	LSB	MSB	00	00	00	00	00	Program fail count
AC	LSB	LSB	MSB	00	00	00	00	00	Erase fail count
C0	LSB	LSB	MSB	00	00	00	00	00	Unexpected Power Loss

									Count
C2	LSB			MIN		MAX	00	00	Temperature
E5		ID 0	ID 1	ID 2	ID 3	ID 4	ID 5		Flash ID
EB			MSB	LSB	MSB	LSB	MSB	LSB	Later bad block info (Read/Write/Erase)
F1	00	LSB			MSB	00	00	00	Total LBA written(LBA=32MB)
F2	00	LSB			MSB	00	00	00	Total LBA read(LBA=32MB)

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